

REMARKS

The present Amendment amends claims 17, 18, 21-23, 25, 27, 28, 31, 32 and 35-37 and leaves claims 19, 20, 24, 26, 29, 30, 34 and 38-40 unchanged. Therefore, the present application has pending claims 17-40.

Amendments were made to each of the claims so as to change the term "machine cycle" to the term "fetch cycle" so as to more accurately describe the intended invention as described and illustrated, for example, in Fig. 11 of the present application which shows that the instruction fetch (IF) stage is conducted so as to fetch at least two instructions. Thus, the term used by the specification "fetch" describes a particular stage of cycle during which the fetching of a plurality of instructions is performed. Accordingly, the more accurate term to use in each of the claims is "fetch cycle" rather than "machine cycle". Each of the claims was amended in this regard.

Applicants submit that the above described amendments being made to each of the claims do not raise any new issues nor change the scope of the claims in any significant manner that would require further consideration and/or search on the part of the Examiner. Thus, entry of these amendments is respectfully requested.

Claims 23, 24 and 26 stand rejected under 35 USC §103(a) as being unpatentable over DeGroot (U.S. Patent No. 4,766,564) in view of Chevillat (U.S. Patent No. 4,615,004). This rejection is traversed for the following reasons. Applicants submit that the features of the present invention as now more clearly recited in claims 23, 24 and 26 are not taught or suggested by DeGroot or Chevillat whether taken individually or in combination with each

other as suggested by the Examiner. Therefore, Applicants respectfully request the Examiner to reconsider and withdraw this rejection.

Amendments were made to each of claims 23, 24 and 26 to more clearly describe features of the present invention. Particularly, amendments were made to each of claims 23, 24 and 26 to recite that the present invention is directed to a data processor having a sequencer, register file and arithmetic operation units, wherein the register file has a bypass circuit for transferring data from an output of a first arithmetic unit to the register file, via a first plurality of signal lines, and for transferring data from the register file to an input of a second arithmetic operation unit, via a second plurality of signal lines, in order that a first operation and a second operation are executed consecutively, and that a result of the first operation is used for the second operation.

The above described features now more clearly recited in the claims causes claim 23 to recite features similar to that of the allowed claims 17-22 and 27-40. Accordingly, the same reasons for allowance as set forth by the Examiner with respect to allowed claims 17-22 and 27-40 apply as well to amended claim 23 and the claims which depend from amended claim 23, namely claims 24 and 26. In making such amendment, Applicants relied upon the statement by the Examiner in paragraph 4 of the Office Action wherein the Examiner states that claims 17-22 and 27-40 are allowable over the prior art of record for reciting the details of the connections of the arithmetic units and bypass circuit with signals in the register file and that said details were not recited in claims 23, 24 and 26. Applicants submit that claims 23, 24 and 26 now recite said details.

Therefore, based on paragraph 4 of the Office Action, both DeGroot and Chevillat suffer from the same deficiencies relative to the features of the present invention as now more clearly recited in the claims and as such when combined does not render obvious the claimed invention. Accordingly, reconsideration and withdrawal of the 35 USC §103(a) rejection of claims 23, 24 and 26 is respectfully requested.

The remaining references of record have been studied. Applicants submit that they do not supply any of the deficiencies noted above with respect to the references utilized in the rejection of claims 23, 24 and 26.

Applicants acknowledge the Examiner's indication in the Office Action that claims 17-22 and 27-40 are allowed.

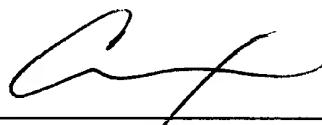
Applicants also acknowledge the Examiner's indication in paragraph 8 of the Office Action that claim 25 would be allowable if rewritten in independent form including all the limitations of the base claim and any intervening claims. Amendments were made to claim 25 to place it in independent form including all the limitations of the base claim and any intervening claims. Therefore, claim 25 is allowable as indicated by the Examiner.

In view of the foregoing amendments and remarks, applicants submit that claims 17-40 are in condition for allowance. Accordingly, early allowance of claims 17-40 is respectfully requested.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, or credit any overpayment of fees, to the deposit account of MATTINGLY, STANGER, MALUR & BRUNDIDGE, P.C., Deposit Account No. 50-1417 (500.28166CX2).

Respectfully submitted,

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